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Application Number

10790.907

**Filing Date**

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**First Named Inventor**

Thomas, et al.

**Art Unit**

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**Examiner Name**

~~TBD~~ Bac H. Au

Attorney Docket Number

2003 P 54322 US

Examiner  
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KIM, G., "Pad-On Circuit (PoC) Process and Its Application," 10th Annual International KGD Packaging & Test Workshop, Napa, CA (September 8-10, 2003) 27 pages.

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07/07/2006

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